CLAIMS

What is claimed is:

1. A method comprising:
requesting a first deferred procedure call for a first interrupt event;
requesting at least one other deferred procedure call for a second interrupt event;
assigning the first deferred procedure call and the at least one other deferred procedure call to a resource;
processing the first interrupt event with the first deferred procedure call; and
processing the second interrupt event with the at least one other deferred procedure call.

2. The method of claim 1, further comprising:
assigning the first deferred procedure call and the at least one other deferred procedure call to a resource comprising a processor exhibiting a single thread of execution; and
executing the first deferred procedure call and the at least one other deferred procedure call on the single thread.

3. The method of claim 1, further comprising:
assigning the first deferred procedure call and the at least one other deferred procedure call to a resource comprising a processor exhibiting a plurality of threads; and
executing the first deferred procedure call on one thread of the plurality of threads while executing the at least one other deferred procedure call on another thread of the plurality of threads.

4. The method of claim 1, further comprising:
assigning the first deferred procedure call to a resource comprising a first thread of a processor;
assigning the at least one other deferred procedure call to a resource comprising a second thread of the processor; and
executing the first deferred procedure call on the first thread while executing the at least one other deferred procedure call on the second thread.

5. The method of claim 1, further comprising:
assigning the first deferred procedure call and the at least one other deferred procedure call to a resource comprising a multi-processor system; and
executing the first deferred procedure call on one processor of the multi-processor system while executing the at least one other deferred procedure call on another processor of the multi-processor system.

6. The method of claim 1, further comprising:
assigning the first deferred procedure call to a resource comprising a first processor;
assigning the at least one other deferred procedure call to a resource comprising a second processor; and
executing the first deferred procedure call on the first processor while executing the at least one other deferred procedure call on the second processor.

7. The method of claim 1, further comprising processing another interrupt event with one of the first deferred procedure call and the at least one other deferred procedure call.

8. A method comprising:
requesting a first deferred procedure call for a first interrupt event;
requesting at least one other deferred procedure call for a second interrupt event; and
processing the first interrupt event with the first deferred procedure call while processing the second interrupt event with the at least one other deferred procedure call.

9. The method of claim 8, further comprising:
executing the first deferred procedure call on a first thread of a processor; and
executing the at least one other deferred procedure call on a second thread of the processor.

10. The method of claim 8, further comprising:
executing the first deferred procedure call on a first processor; and
executing the at least one other deferred procedure call on a second processor.

11. The method of claim 8, further comprising processing another interrupt event with one of the first deferred procedure call and the at least one other deferred procedure call.

12. A driver comprising:
an interrupt handler to identify interrupt events; and
at least two deferred procedure calls, each of the at least two deferred procedure calls to process at least one of the interrupt events.

13. The driver of claim 12, the interrupt handler to assign the at least two deferred procedure calls to a resource for execution.

14. The driver of claim 12, the interrupt handler to assign one of the at least two deferred procedure calls to a first resource for execution and another of the at least two deferred procedure calls to a second resource for execution.

15. A computer system comprising:

a driver stored in a memory of the computer system, the driver including

an interrupt handler to identify interrupt events; and

at least two deferred procedure calls, each of the at least two deferred procedure calls to process at least one of the interrupt events.

and

a processor to execute the at least two deferred procedure calls.

16. The computer system of claim 15, the interrupt handler to assign the at least two deferred procedure calls to a single thread exhibited by the processor for execution.

17. The computer system of claim 15, the interrupt handler to assign a first of the at least two deferred procedure calls to one thread of the processor and another of the at least two deferred procedure calls to a second thread of the processor for execution.

18. The computer system of claim 15, the interrupt handler to assign one of the at least two deferred procedure calls to the processor and another of the at least two deferred procedure calls to a second processor for execution.

19. The computer system of claim 15, further comprising at least one peripheral device, the interrupt events associated with the at least one peripheral device.

20. An article of manufacture comprising:
a machine accessible medium, the machine accessible medium providing instructions that, when executed by a machine, cause the machine to:
request a first deferred procedure call for a first interrupt event;
request at least one other deferred procedure call for a second interrupt event;
assign the first deferred procedure call and the at least one other deferred procedure call to a resource;
process the first interrupt event with the first deferred procedure call; and
process the second interrupt event with the at least one other deferred procedure call.

21. The article of claim 20, wherein the instructions, when executed, further cause the machine to:
assign the first deferred procedure call and the at least one other deferred procedure call to a resource comprising a processor exhibiting a single thread of execution; and
execute the first deferred procedure call and the at least one other deferred procedure call on the single thread.

22. The article of claim 20, wherein the instructions, when executed, further cause the machine to:
assign the first deferred procedure call and the at least one other deferred procedure call to a resource comprising a processor exhibiting a plurality of threads; and
execute the first deferred procedure call on one thread of the plurality of threads while executing the at least one other deferred procedure call on another thread of the plurality of threads.

23. The article of claim 20, wherein the instructions, when executed, further cause the machine to:

assign the first deferred procedure call to a resource comprising a first thread of a processor;

assign the at least one other deferred procedure call to a resource comprising a second thread of the processor; and

execute the first deferred procedure call on the first thread while executing the at least one other deferred procedure call on the second thread thread.

24. The article of claim 20, wherein the instructions, when executed, further cause the machine to:

assign the first deferred procedure call and the at least one other deferred procedure call to a resource comprising a multi-processor system; and

execute the first deferred procedure call on one processor of the multi-processor system while executing the at least one other deferred procedure call on another processor of the multi-processor system.

25. The article of claim 20, wherein the instructions, when executed, further cause the machine to:

assign the first deferred procedure call to a resource comprising a first processor;

assign the at least one other deferred procedure call to a resource comprising a second processor; and

execute the first deferred procedure call on the first processor while executing the at least one other deferred procedure call on the second processor.

26. The article of claim 20, wherein the instructions, when executed, further cause the machine to process another interrupt event with one of the first deferred procedure call and the at least one other deferred procedure call.

27. An article of manufacture comprising:
a machine accessible medium, the machine accessible medium providing instructions that, when executed by a machine, cause the machine to:
request a first deferred procedure call for a first interrupt event;
request at least one other deferred procedure call for a second interrupt event; and
process the first interrupt event with the first deferred procedure call while processing the second interrupt event with the at least one other deferred procedure call.

28. The article of claim 27, wherein the instructions, when executed, further cause the machine to:
execute the first deferred procedure call on a first thread of a processor; and
execute the at least one other deferred procedure call on a second thread of the processor.

29. The article of claim 27, wherein the instructions, when executed, further cause the machine to:
execute the first deferred procedure call on a first processor; and
execute the at least one other deferred procedure call on a second processor.

30. The article of claim 27, wherein the instructions, when executed, further cause the machine to process another interrupt event with one of the first deferred procedure call and the at least one other deferred procedure call.

add A'